



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

Application No.: 09/779,096) Date: October 24, 2003
Applicant: Shi-Tron LIN, et al.) Examiner: Ori Nadav
Filed: 02/08/2001) Group Art Unit 2811
For: "Output Buffer with Good ESD) Our Ref: B-4101
Protection") (618582-4/RPB:TGC)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This paper responds to the Office Action dated June 24, 2003. A petition for a one month extension of time to respond is attached, along with a check for the fee therefor. Please amend the above-identified application as described below. **All remarks herein are made without prejudice.**

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 15 of this paper.

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02 FC:1202

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